

## STANDARD TEMPLATE OF FACULTY PROFILE FOR UPLOADING OF UNIVERSITY WEBSITE

Title	Dr.	First Name	Amit Prakash	Last Name	Singh	
Designation		Professor				
School /Dept. Name		University School of Information, Communication & Technology				
Address:		Room No. – EFR-402 USICT Guru Gobind Singh Indraprastha University Sector-16C, Dwarka New Delhi – 110 078				
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Web Page (if any)						
Subjects Taught		<ul style="list-style-type: none"> <li>• Digital System Design</li> <li>• Artificial Intelligence</li> <li>• Machine Learning</li> </ul>				
Areas of Interest/ Specialization		<ul style="list-style-type: none"> <li>• IoT</li> <li>• ANN</li> <li>• FPGA based Design</li> <li>• Machine Learning</li> <li>• Application of Deep Learning</li> </ul>				
Experience (in years)		Total	20 years			
		Industry	NIL			
		Teaching	20 Years			
		Research	7 years			
Educational Qualifications		UG	B.Sc. (Electronics)			
		PG	M.Sc. (Electronics) M.Tech. (Computer Science & Engineering)			
		Doctorate	Ph.D. (Information Technology)			
		Any other – Diploma in IPR	----			

Research Publications in Journals (last 5 years)	25			
Papers Published in Conference Proceedings (last 5 years)	29			
Books Authored/ Book Volume Chapters	2			
No. of Conferences	National	Attended		Organized
		2		1
	International	7		2
Research Guidance	Awarded	PG (M.Tech.)	M. Phil	Doctorate
		20	----	5
	Undergoing	2	----	8
Research Projects	Completed	1		
	Undergoing	1		
Awards & Distinctions	UGC-NET qualified in Electronic Science			
Administrative Assignments Handled	<ol style="list-style-type: none"> <li>1. Director, IPU Innovation &amp; Incubation Foundation</li> <li>2. PhD Programme Coordinator, USICT</li> <li>3. Chairman, University Center for IT Infrastructure Management &amp; Services (2012-14)</li> <li>4. Chairman, Procurement Committee (GGSIPU-East Campus) -FY 2021-22</li> <li>5. Coordinator, M.Tech. (Weekend Programme) – (AY 2014-17)</li> <li>6. GGSIPU-CET convener (Result Preparation) (2006-08)</li> <li>7. Coordinator, Visvesvaraya PhD Scheme, funded by Ministry of IT &amp; Electronics, (2015-2021)</li> <li>8. University Coordinator, AICTE Doctoral Fellowship</li> </ol>			
Association with Professional Bodies	<ol style="list-style-type: none"> <li>1. IEEE</li> <li>2. ACM</li> <li>3. CSI</li> </ol>			
Any other Achievements	1. Developed VHDL and Embedded System Design Lab			